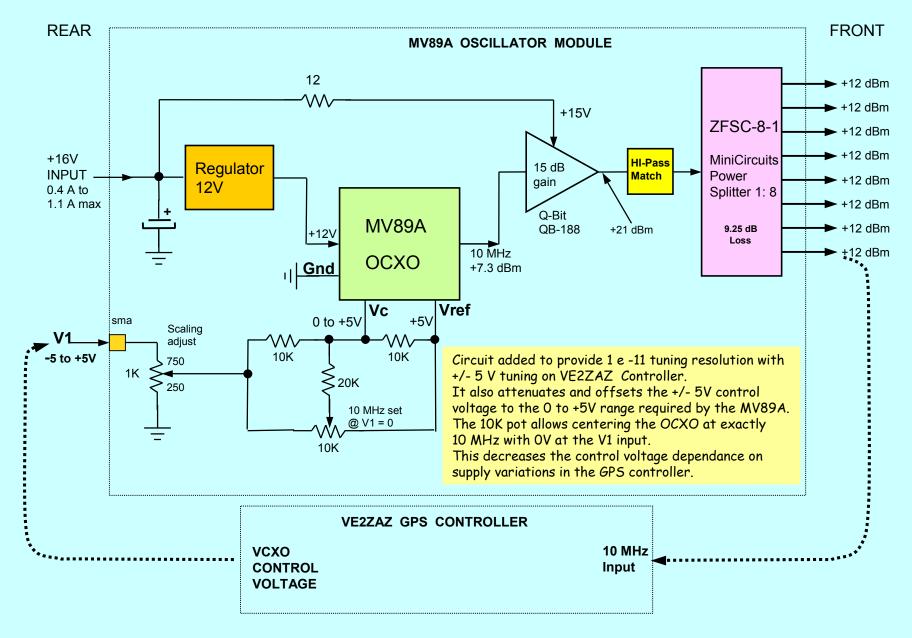
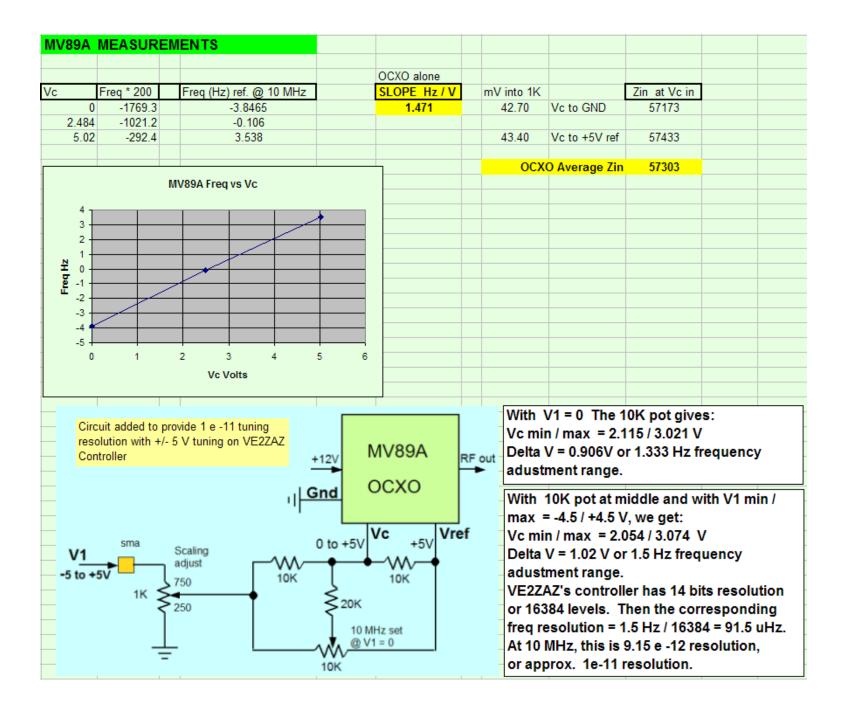
OCXO AND CLOCK DISTRIBUTION SYSTEM

As used with VE2ZAZ Controller

Jacques Audet VE2AZX June 2011 Rev. June 2014

BLOCK DIAGRAM

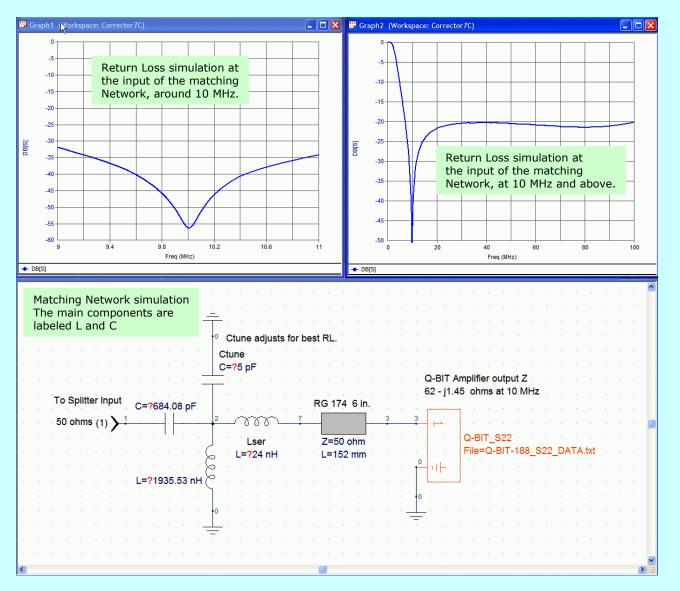


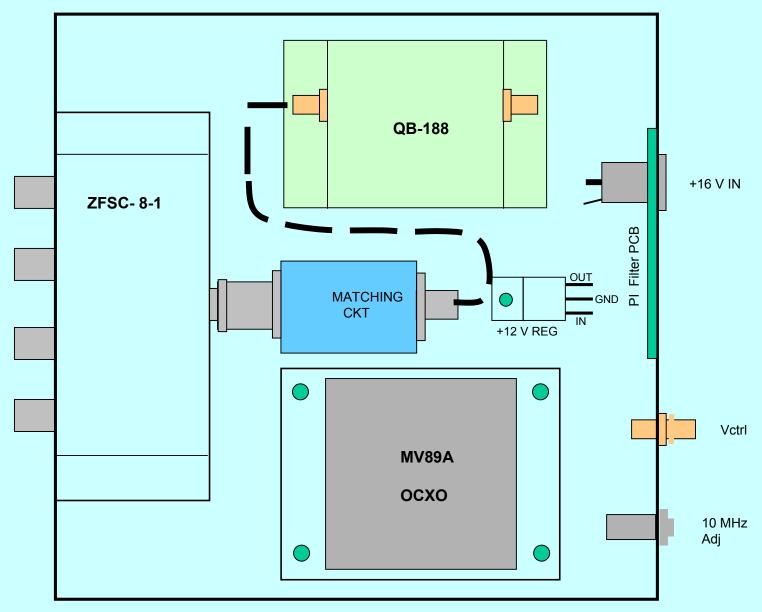


HI PASS MATCHING CIRCUIT AT THE SPLITTER INPUT

The matching circuit transforms the Q-Bit amplifier output Z (62 - j1.45 ohms, measured) to a value very close to 50 ohms at 10 MHz. This MAXIMIZES the port to port isolation of the 1 : 8 splitter.

A high pass matching circuit is used to prevent degradation of the return loss at frequencies above 10 MHz. This also MAXIMIZES the port to port isolation at the 10 MHz harmonics.

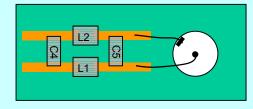


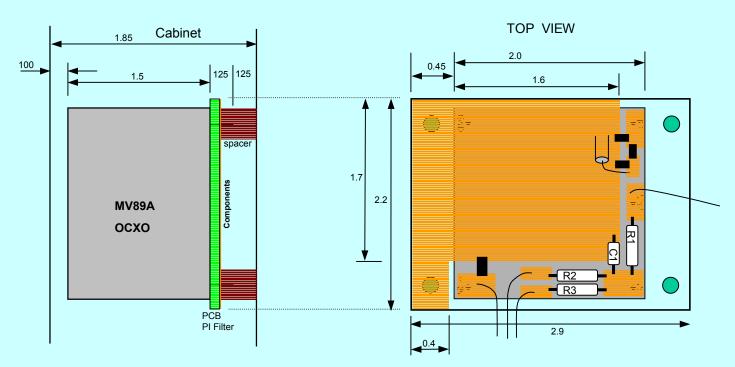


Cabinet: Hammond 1455T1601

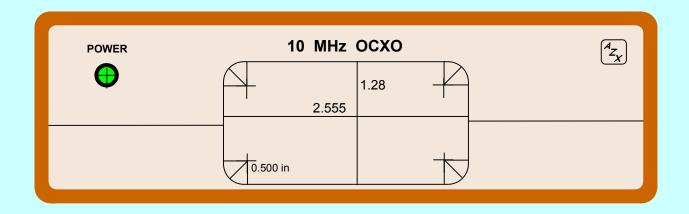
X 2.529

PI Filter PCB

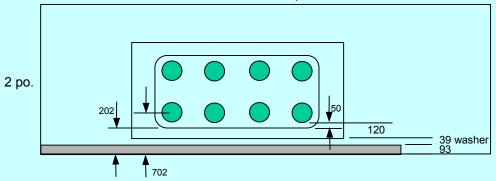


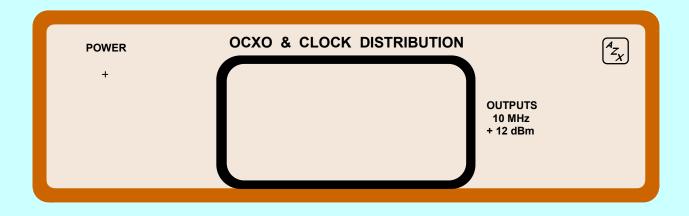


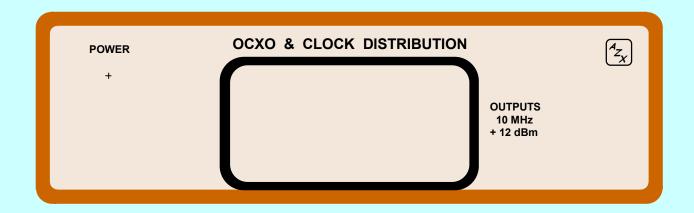
SIDE VIEW

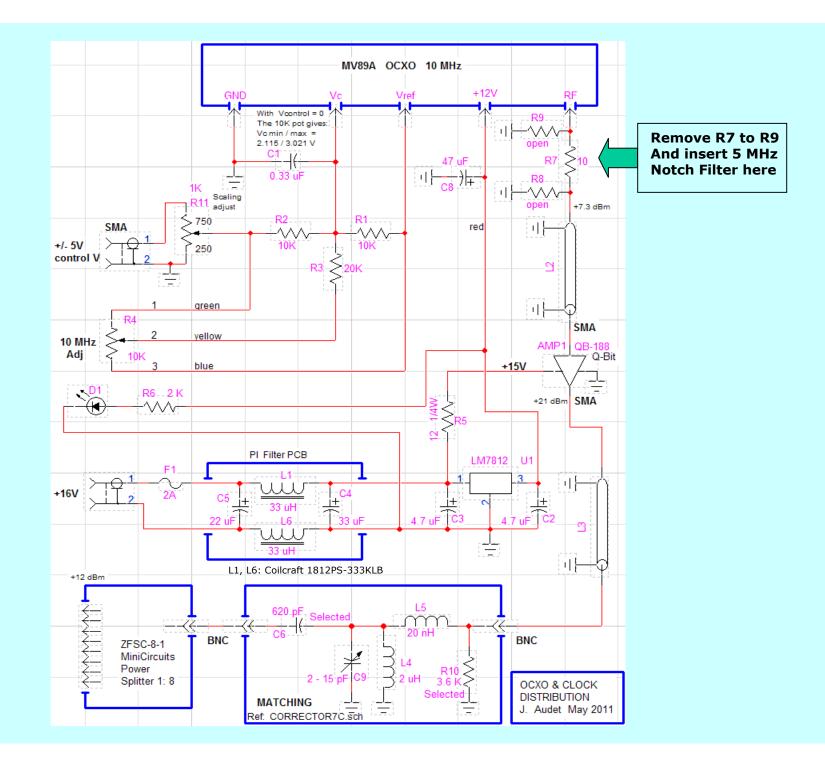






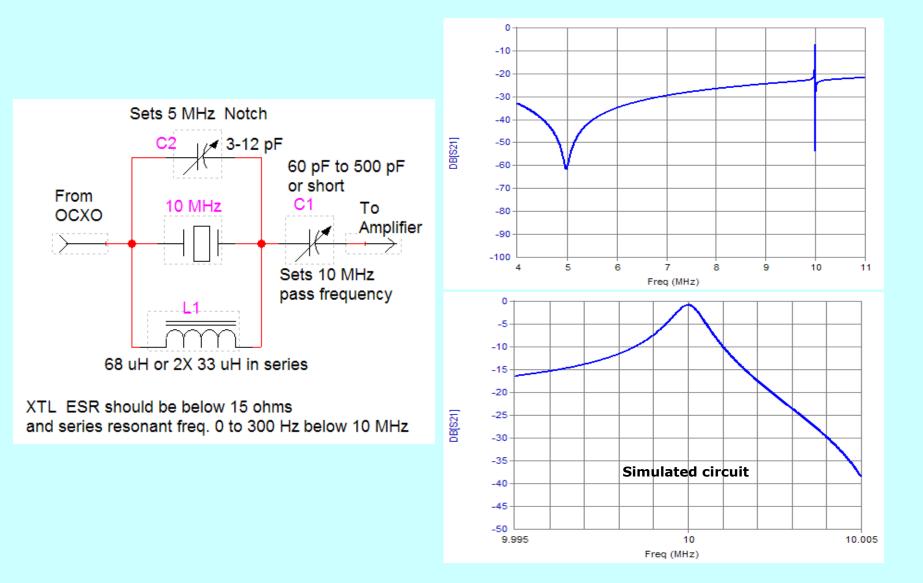


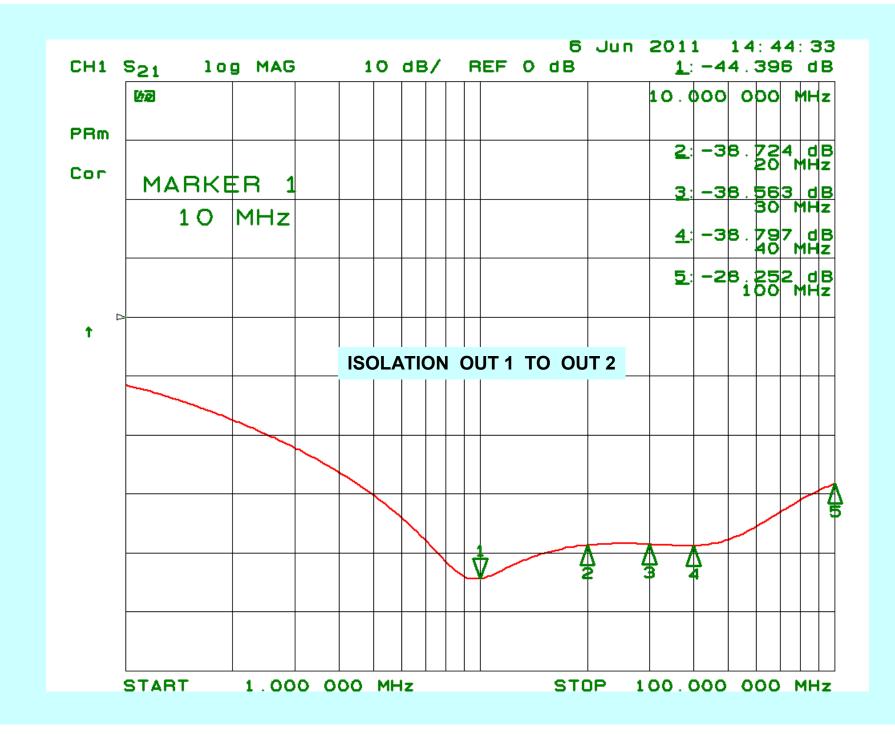


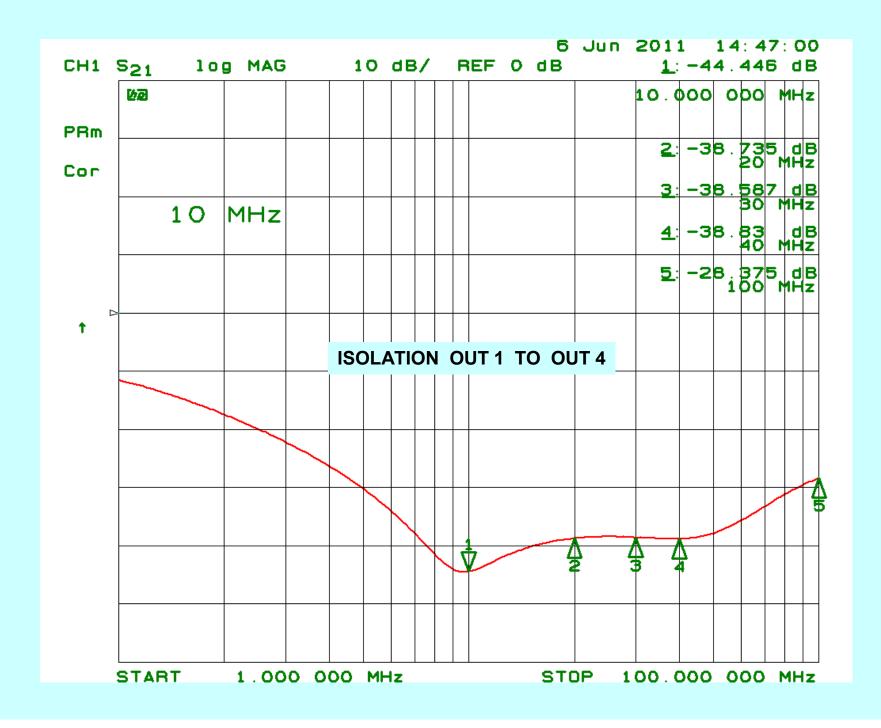


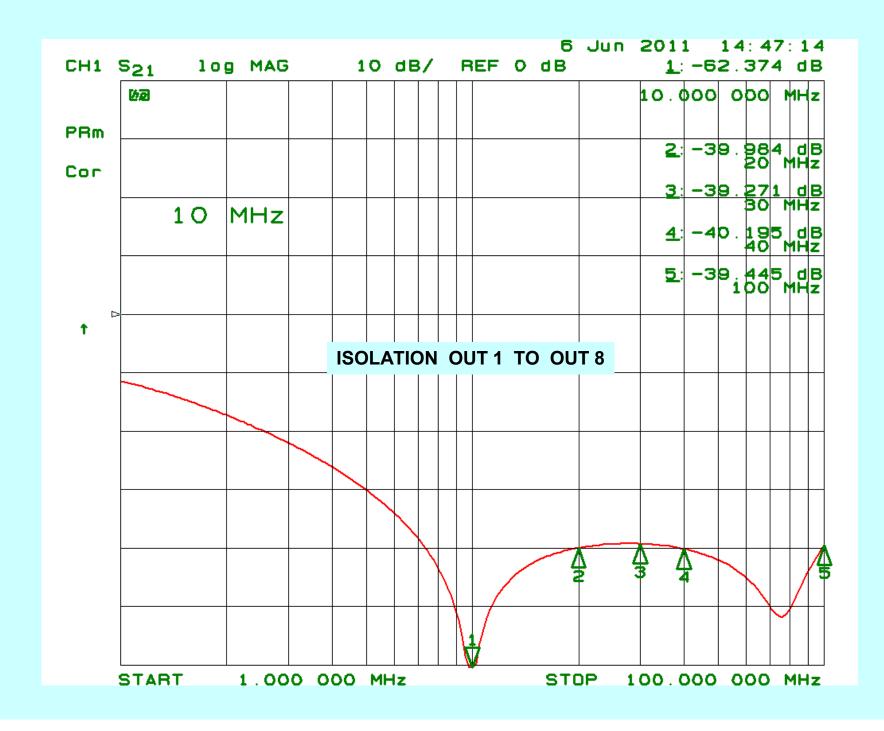
5 MHz Notch Filter

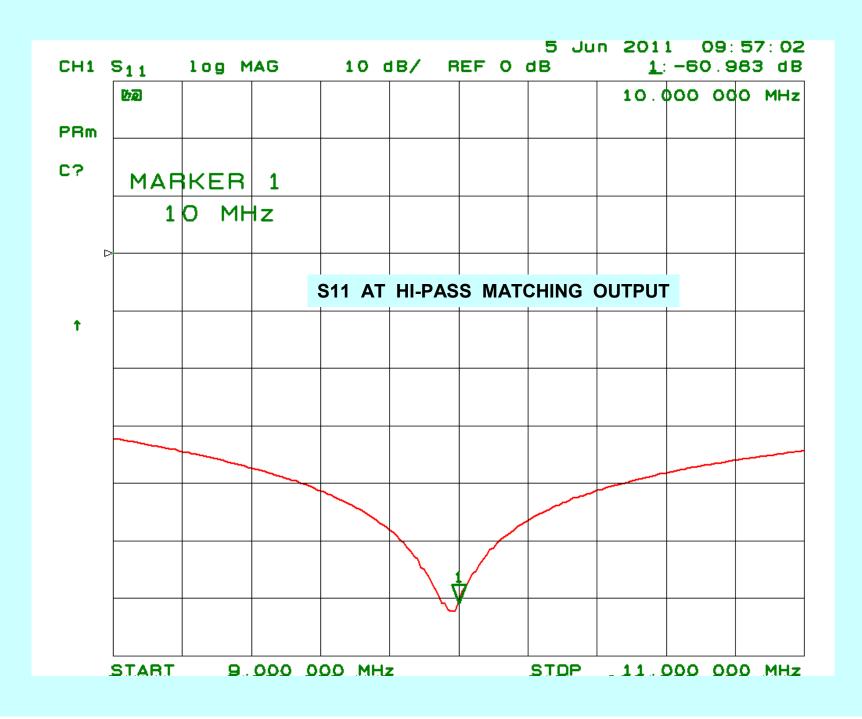
The 5 MHz output was – 45 dB down on my OCXO. See slide 16 This notch filter adds more than 50 dB rejection at 5 MHz

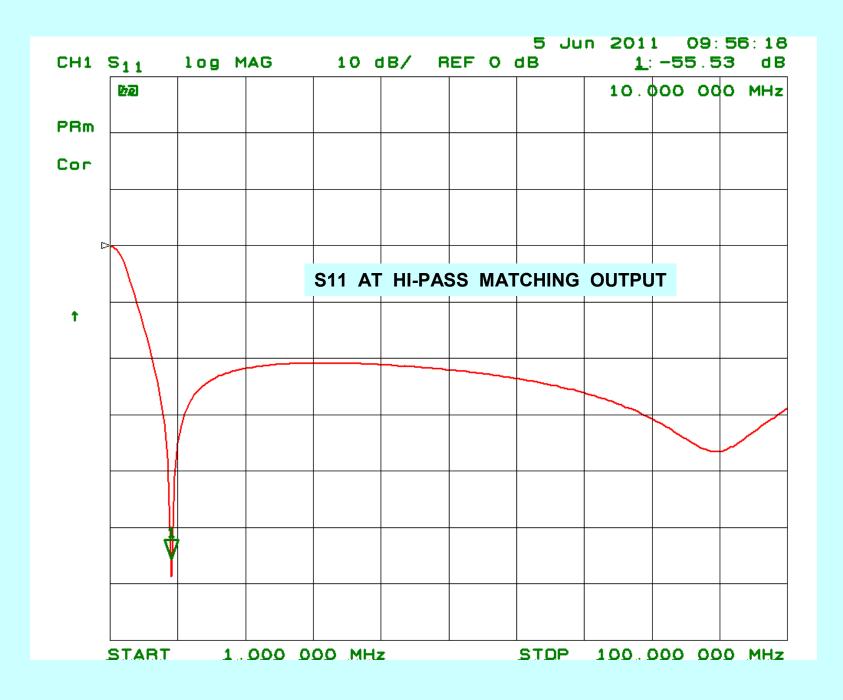


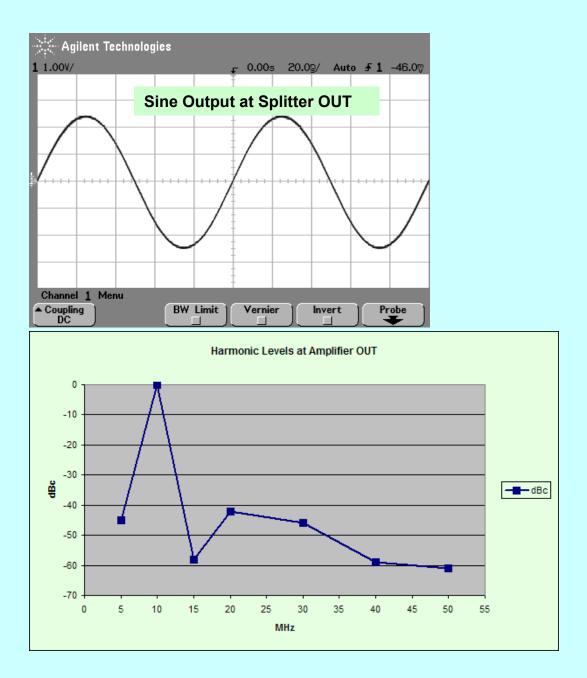


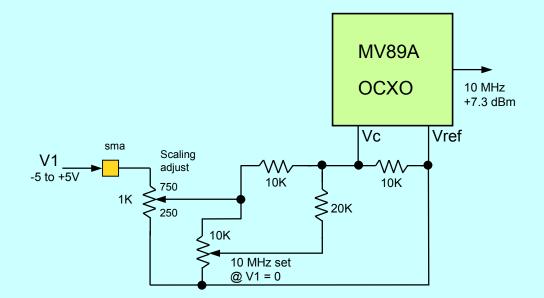




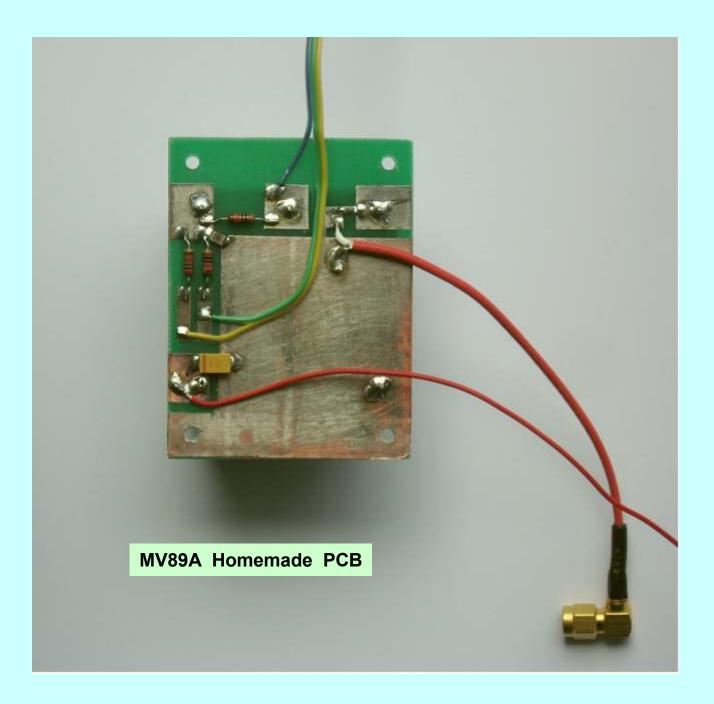


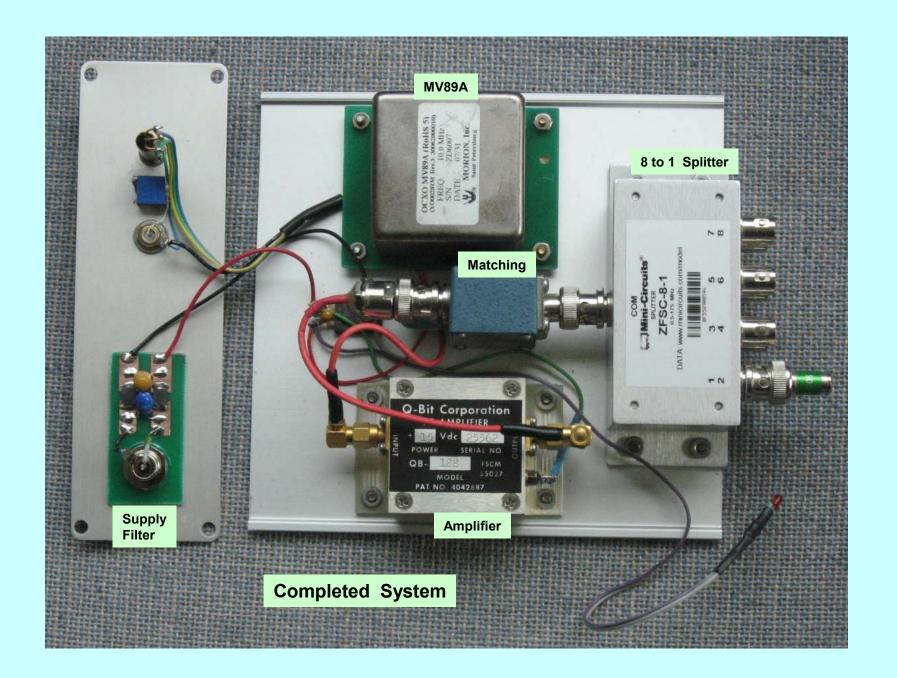






Scaling and Frequency Adjust Circuit









OUTDOOR GPS ANTENNA





